Call for papers

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Advanced Interconnect and Packaging Technologies

Advanced progress in microelectronic, optoelectronic, and nano-electronic devices requires developing new materials and technologies to meet the increasing electrical, thermal, mechanical, reliability, performance, and environmental demands placed on interconnects and packaging at all levels. The special topic will address current research in new and existing materials for advanced interconnect and packaging technologies, including Pb-free and low-temperature solder alloys, Ag and Cu sintering, hybrid bonding, 3D packaging and interconnection, wafer-level packaging, and reliability issues.

Original research papers should be 3,000-9,000 words with up to 12 figures maximum; review papers should be 6,000-11,000 words with up to 20 figures maximum.

Detailed author instructions are available at: http://www.tms.org/AuthorTools/

Keywords for this topic: Advanced Materials; Electronic Materials; Intermetallics; Joining; Thin Films and Interfaces; Reliability

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Committee Sponsor(s): Electronic Packaging and Interconnection Materials

If you are interested in submitting a paper, upload your manuscript at https://www.editorialmanager.com/jomj/

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