

CALL FOR ABSTRACTS

March 10–14, 2019 San Antonio, Texas, USA

SUBMIT AN ABSTRACT TO:

ELECTRONIC MATERIALS

Advanced Microelectronic Packaging, Emerging Interconnection Technology, and Pb-free Solder

Continuing advances in microelectronic, optoelectronic, and nanoelectronic devices require new materials and technologies to meet the increasing electrical, thermal, mechanical, reliability, performance, and environmental demands placed on interconnects and packaging at all levels. This symposium will address current researches in Packaging Materials and Process, including lead-free solder, alternative interconnects, conductive adhesive, epoxy, substrates, 3D packaging, wafer level packaging, quality, reliability, and failure analysis.

ORGANIZERS

Kazuhiro Nogita, University of Queensland, Australia
Tae-Kyu Lee, Portland State University, USA
Yan Li, Intel Corporation, USA
Christopher M. Gourlay, Imperial College London, United Kingdom
Zhi-Quan Liu, Chinese Academy of Sciences, China
Rahul Panat, Carnegie Mellon University, USA
Albert T. Wu, National Central University, Taiwan
André Delhaise, University of Toronto, Canada
Mohd Arif Salleh, Universiti Malaysia Perlis, Malaysia